

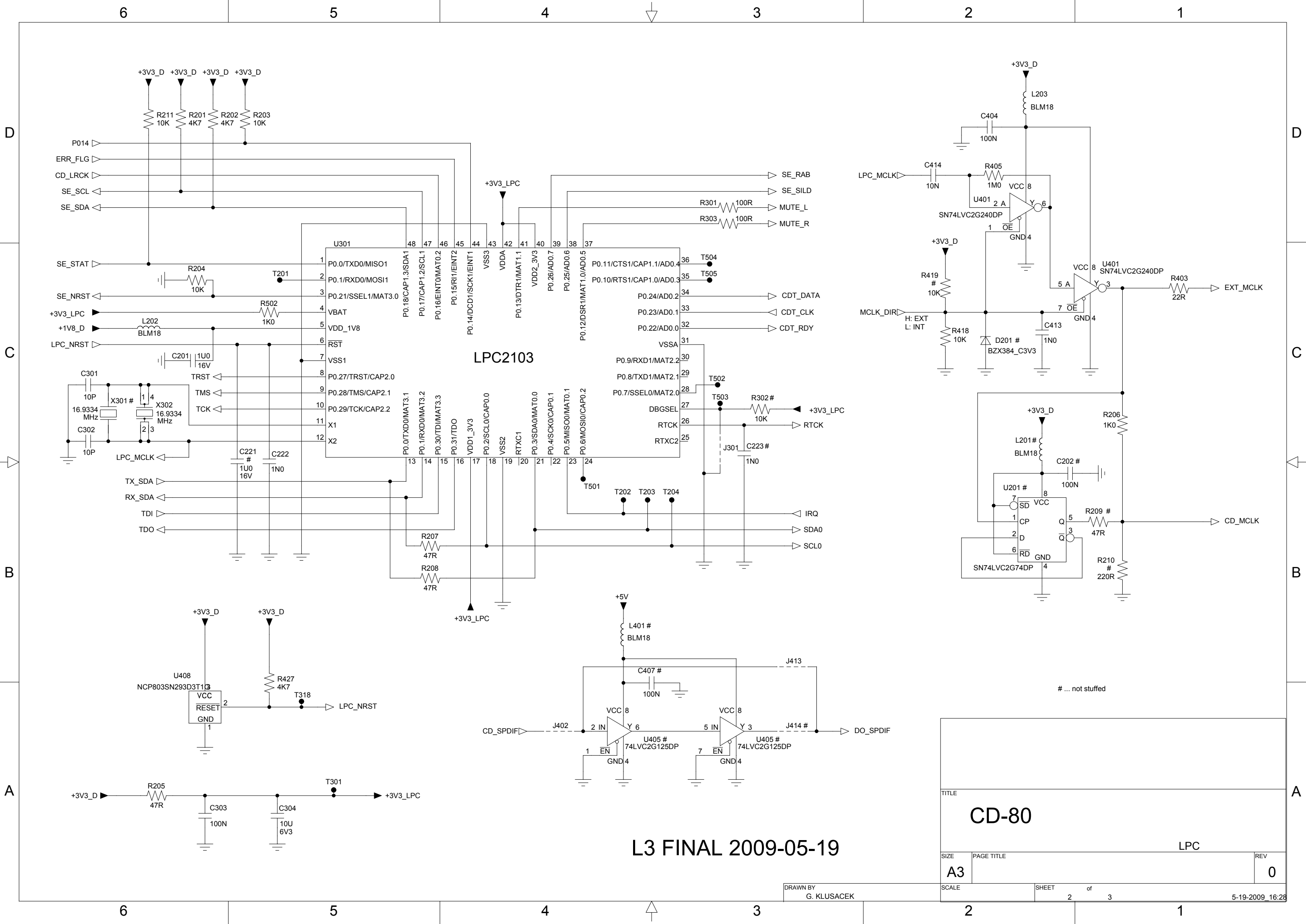
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SIGNAL SERVO PROCESSOR

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2009-05-19

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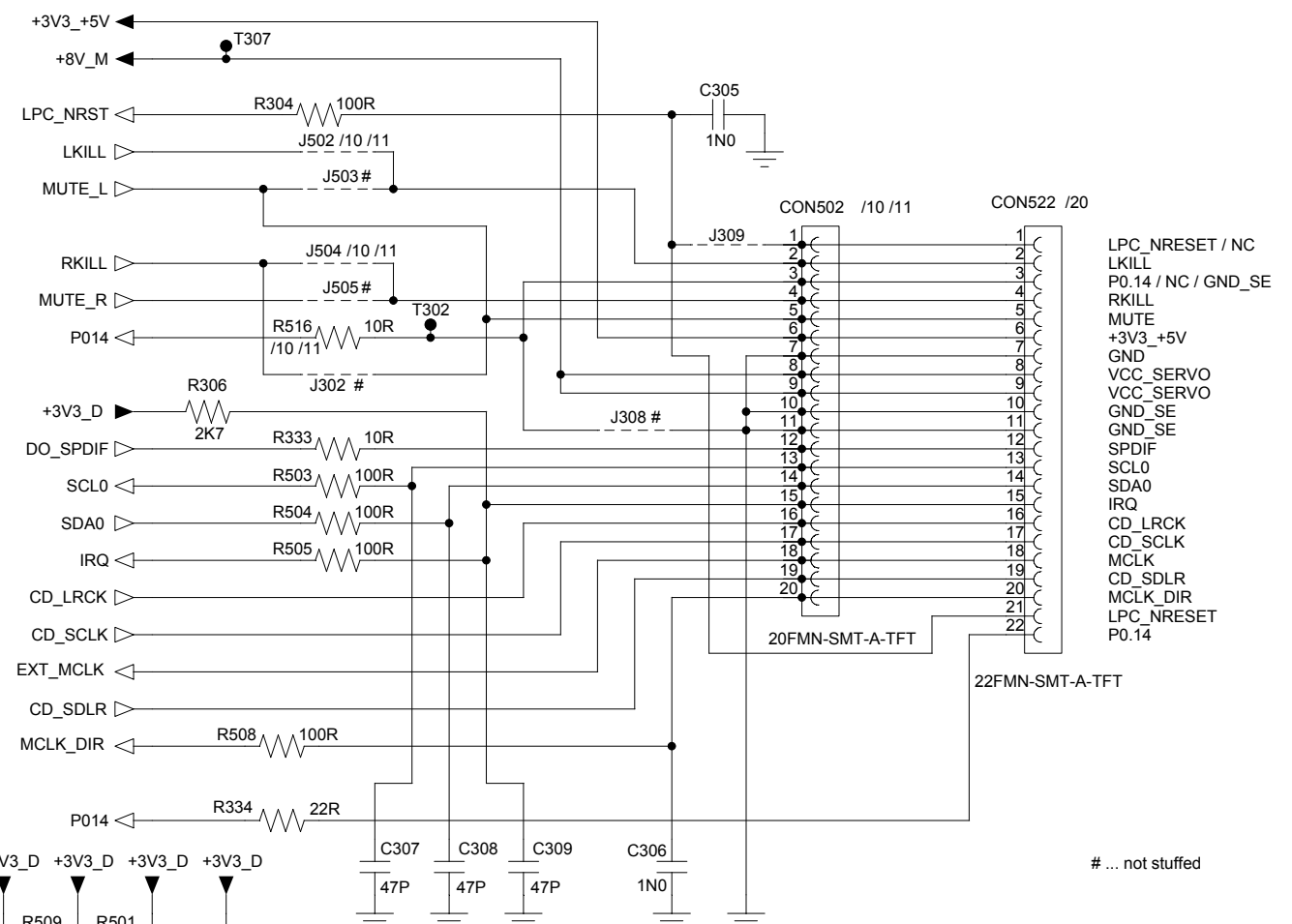
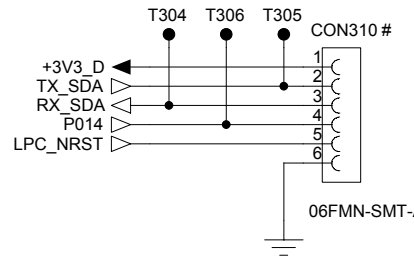
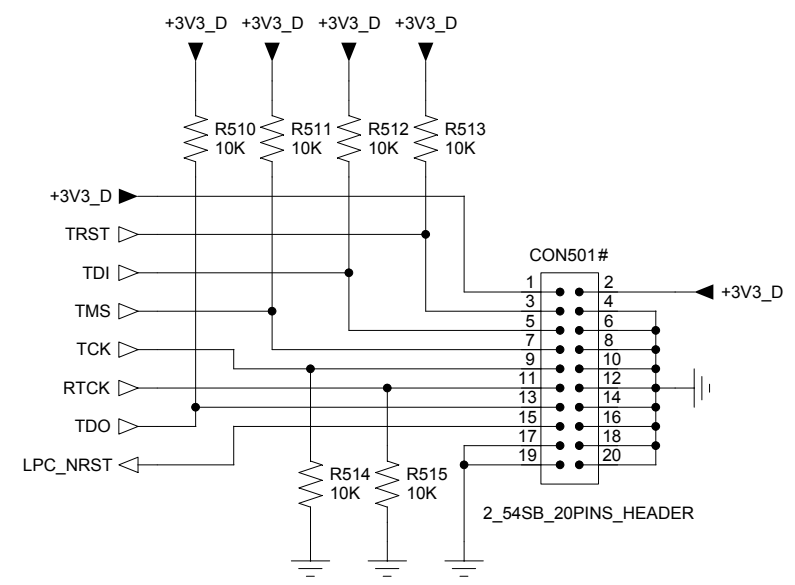
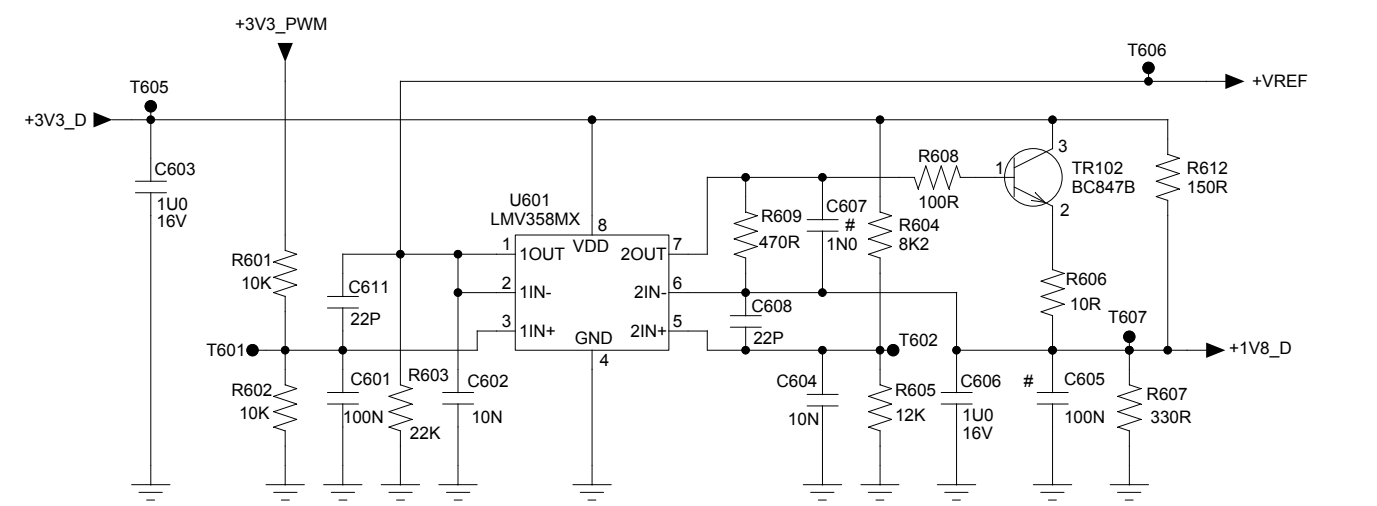
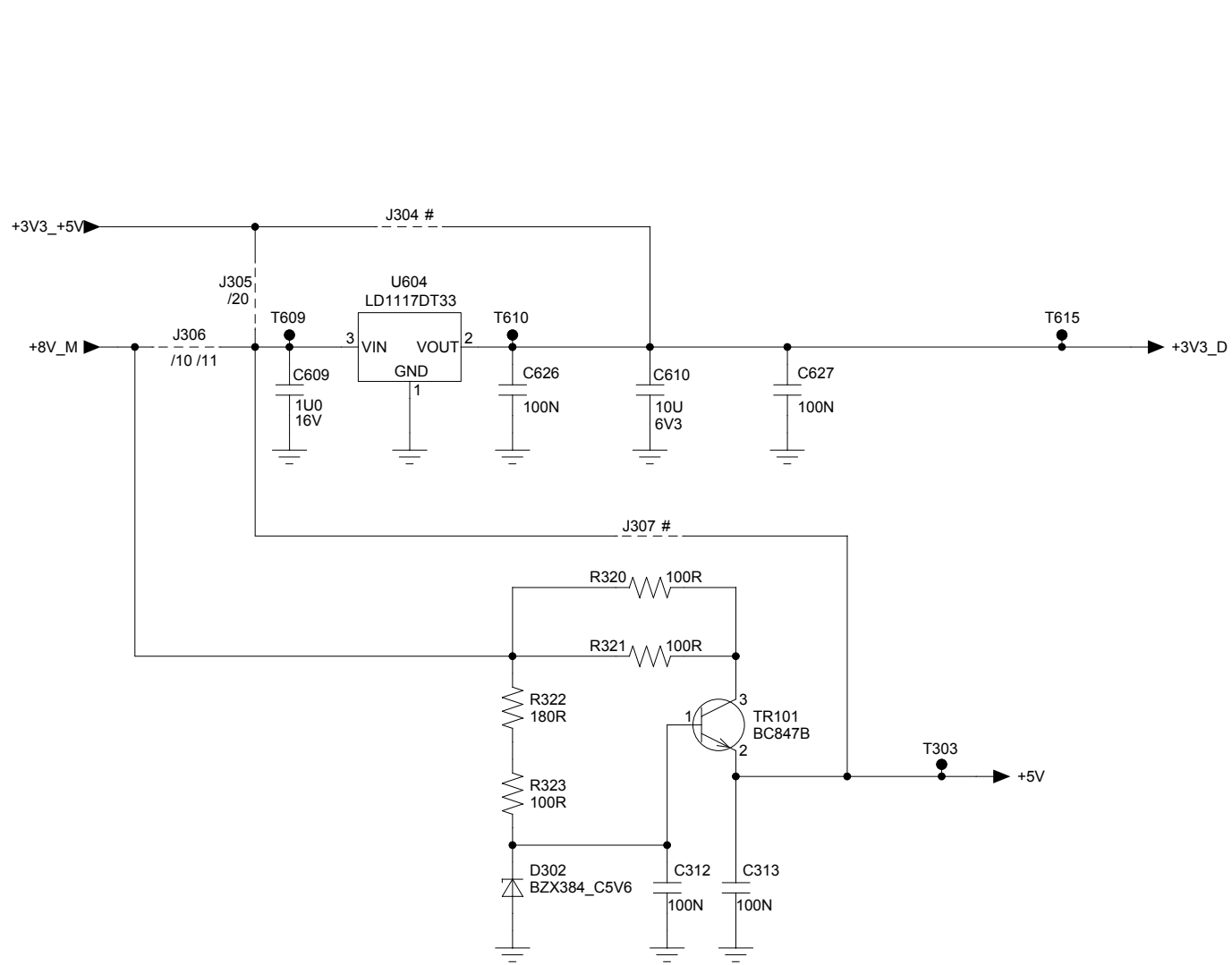
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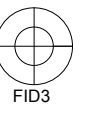
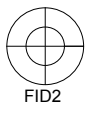
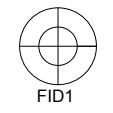
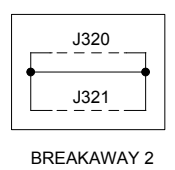
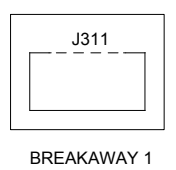
A



- 1 LPC_NRESET / NC
- 2 LKILL
- 3 P0.14 / NC / GND_SE
- 4 RKILL
- 5 MUTE
- 6 +3V3_+5V
- 7 GND
- 8 VCC_SERVO
- 9 VCC_SERVO
- 10 GND_SE
- 11 GND_SE
- 12 SPDIF
- 13 SCL0
- 14 SDA0
- 15 IRQ
- 16 CD_LRCK
- 17 CD_SCLK
- 18 MCLK
- 19 CD_SDLR
- 20 MCLK_DIR
- 21 LPC_NRESET
- 22 P0.14

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